



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 3668

SERIAL NUMBER 10/786,701	FILING DATE 02/24/2004 RULE	CLASS 257	GROUP ART UNIT 2815	ATTORNEY DOCKET NO. JCLA12955
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

APPLICANTS

Chih-Feng Huang, Jhubei City, TAIWAN;
 Ta-Yung Yang, Milpitas, CA;
 Jenn-Yu G. Lin, Taipei, TAIWAN; Tuo-Hsin Chien, Tucheng City, TAIWAN;

** CONTINUING DATA *****
 NONE - LPR

** FOREIGN APPLICATIONS *****
 NONE - NPN

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **
 ** 05/15/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 3	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>WNS</i>	

ADDRESS
 23900
 J C PATENTS, INC.
 4 VENTURE, SUITE 250
 IRVINE , CA
 92618

TITLE
 High voltage and low on-resistance LDMOS transistor having equalized capacitance

FILING FEE RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)